

Achieving the best system cost in Mid/High Power

Mid-Power LED – 3030 Series STW8C2PA (Cool, Neutral, Warm)

















### **Product Brief**

### **Description**

- This White Colored surface-mount LED comes in standard package dimension. Package Size: 3.0x3.0x0.65mm
- It has a substrate made up of a molded plastic reflector sitting on top of a lead frame.
- The die is attached within the reflector cavity and the cavity is encapsulated by silicone.
- The package design coupled with careful selection of component materials allow these products to perform with high reliability.

### **Features and Benefits**

- Thermally Enhanced Package Design
- Mid Power to High Power up to 1.4W
- Max. Driving Current 400mA
- Compact Package Size
- High Color Quality with CRI Min.80(R9>0)
- · RoHS compliant

### **Key Applications**

- Interior lighting
- General lighting
- Indoor and out door displays
- Architectural / Decorative lighting

**Table 1. Product Selection Table** 

Part Number	сст						
Part Number	Color	Min.	Тур.	Max.			
STW8C2PA	Cool White	4700K	5600K	7000K			
STW8C2PA	Neutral White	3700K	4200K	4700K			
STW8C2PA	Warm White	2600K	3000K	3700K			



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## **Performance Characteristics**

Table 2. Product Selection Guide,  $I_F = 200 \text{mA}$ ,  $T_i = 25^{\circ}\text{C}$ , RH30%

	CCT (K) [1]		Luminous	Intensity [2]	Luminou	s Flux <sup>[3]</sup>	CRI
Part Number	CCI (K)	RANK	lv	(cd)	Фv (	(lm)	Ra
	Тур.		Min	Max	Min	Max	Min.
·	_	K21	21.5	24	66.7	74.4	80
	6,500	K24	24	26	74.4	80.6	80
_		K26	26	28.5	80.6	88.3	80
	_	K21	21.5	24	66.7	74.4	80
	5,600	K24	24	26	74.4	80.6	80
		K26	26	28.5	80.6	88.3	80
		K21	21.5	24	66.7	74.4	80
	5,000 -	K24	24	26	74.4	80.6	80
	5,000 -	K26	26	28.5	80.6	88.3	80
		K28	28.5	31.2	88.3	96.7	80
-		K21	21.5	24	65.6	73.2	80
	4,500	K24	24	26	73.2	79.3	80
CTM/OCODA		K26	26	28.5	79.3	86.9	80
STW8C2PA		K21	21.5	24	65.6	73.2	80
	4.000	K24	24	26	73.2	79.3	80
	4,000	K26	26	28.5	79.3	86.9	80
	_	K28	28.5	31.2	86.9	95.2	80
-		K21	21.5	24	64.5	72	80
	3,500	K24	24	26	72	78	80
	_	K26	26	28.5	78	85.5	80
-		K21	21.5	24	64.5	72	80
	3,000	K24	24	26	72	78	80
	-	K26	26	28.5	78	85.5	80
-		K21	21.5	24	64.5	72	80
	2,700	K24	24	26	72	78	80
	-	K26	26	28.5	78	85.5	80

#### Notes:

- (1) Correlated Color Temperature is derived from the CIE 1931 Chromaticity diagram.
- (2) Seoul Semiconductor maintains a tolerance of  $\pm 7\%$  on Intensity and power measurements. The luminous intensity IV was measured at the peak of the spatial pattern which may not be aligned with the mechanical axis of the LED package.
- (3) The lumen table is only for reference.

### **Performance Characteristics**

Table 3. Characteristics, I<sub>F</sub> = 200mA, T<sub>i</sub> = 25°C, RH30%

Dovometor	Cumbal		Value		Unit
Parameter	Symbol	Min.	Тур.	Max.	Onit
Forward Current	l <sub>F</sub>	-	200	-	mA
Forward Voltage <sup>[1]</sup>	$V_{F}$	2.9	-	3.4	V
Luminous Intensity (5000K) <sup>[1]</sup>	I <sub>v</sub> (200mA)	-	26.3 (81.5)	-	cd
Luminous Intensity (3,500K) <sup>[1]</sup>	I <sub>v</sub> (200mA)	-	25.9 (77.7)	-	(lm)
CRI <sup>[1]</sup>	$R_a$	80	-	90	
Viewing Angle [2]	2Θ <sub>1/2</sub>	-	120	-	Deg.
Thermal resistance (J to S) [3]	Rθ <sub>J-S</sub>	-	10	-	°C/W
ESD Sensitivity(HBM)	-		Class 3A JES	D22-A114-E	

**Table 4. Absolute Maximum Ratings** 

Parameter	Symbol	Value	Unit
Forward Current	I <sub>F</sub>	400	mA
Power Dissipation	$P_{D}$	1.44	W
Junction Temperature	T <sub>j</sub>	125	°C
Operating Temperature	T <sub>opr</sub>	-40~ + 85	°C
Storage Temperature	T <sub>stg</sub>	-40 ~ + 100	°C

#### Notes:

(1) Tolerance : VF : $\pm$ 0.2V, IV : $\pm$ 7%, Ra : $\pm$ 2, x,y : $\pm$ 0.005

(2)  $2\Theta_{1/2}$  is the off-axis where the luminous intensity is 1/2 of the peak intensity

(3) Thermal resistance: Rth<sub>JS</sub> (Junction to Solder)

(4) A zener diode is included to protect the product from ESD.

- LED's properties might be different from suggested values like above and below tables if operation condition will be exceeded our parameter range. Care is to be taken that power dissipation does not exceed the absolute maximum rating of the product.
- All measurements were made under the standardized environment of Seoul Semiconductor.
- Thermal resistance can be increased substantially depending on the heat sink design/operating condition, and the maximum possible driving current will decrease accordingly.

Fig 1. Color Spectrum,  $I_F = 200$ mA,  $T_j = 25$ °C, RH30%

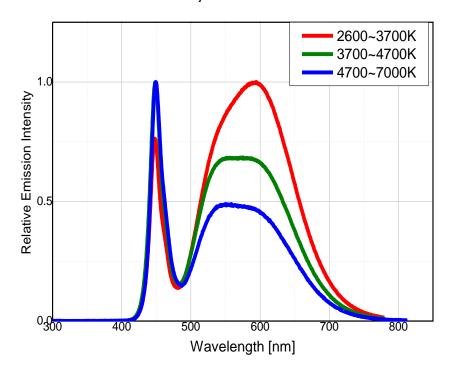


Fig 2. Viewing Angle Distribution,  $I_F = 200 \text{mA}$ 

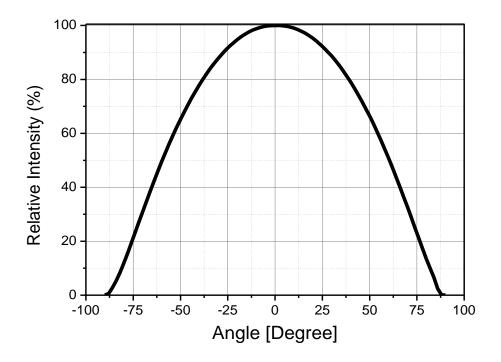


Fig 3. Forward Voltage vs. Forward Current, T<sub>i</sub> = 25°C

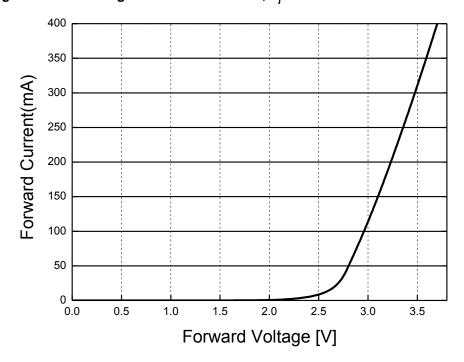


Fig 4. Forward Current vs. Relative Luminous Flux, T<sub>i</sub> = 25°C

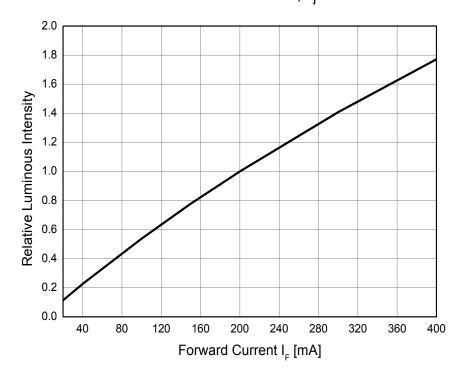
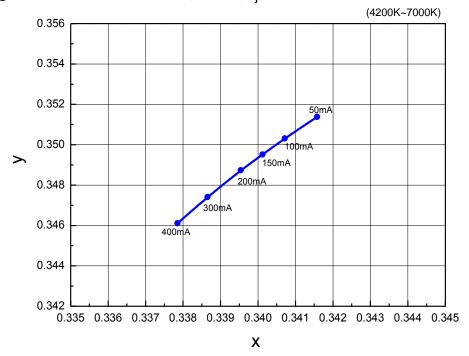


Fig 5. Forward Current vs. CIE X, Y Shift, T<sub>i</sub> = 25°C



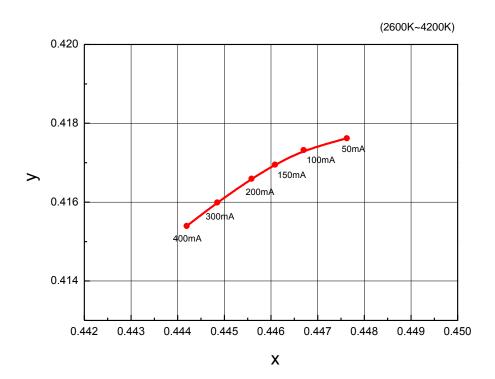


Fig 6. Relative Light Output vs. Junction Temperature,  $I_F = 200 \text{mA}$ 

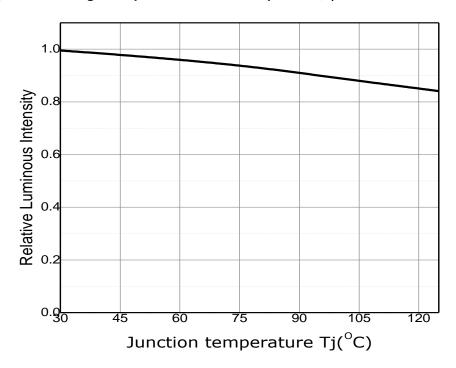


Fig 7. Junction Temperature vs. Relative Forward Voltage,  $I_F = 200 \text{mA}$ 

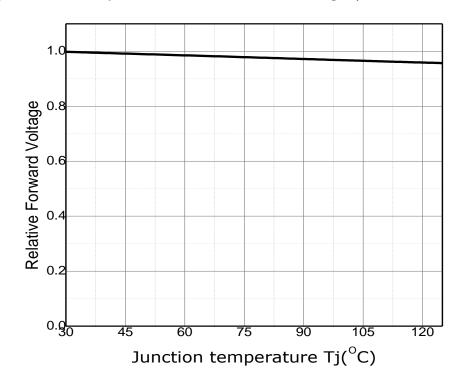
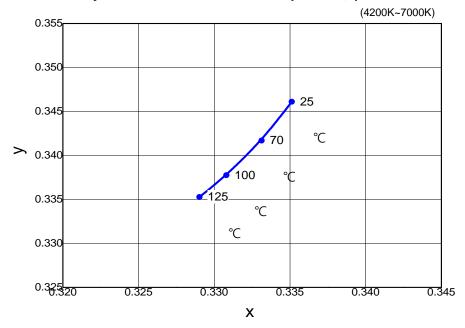


Fig 8. Chromaticity Coordinate vs. Junction Temperature,  $I_F = 200 \text{mA}$ 



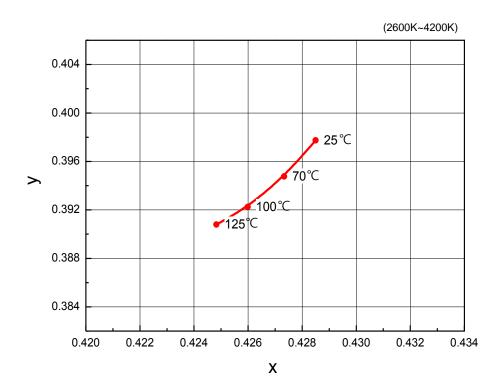
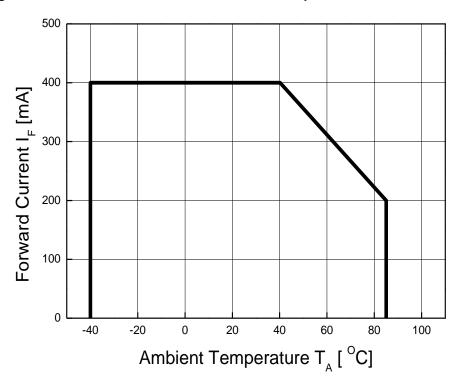


Fig 9. Maximum Forward Current vs. Ambient Temperature



## **Color Bin Structure**

Table 4. Bin Code description, T<sub>j</sub>=25 ℃, I<sub>F</sub>=200mA

Part Number	Luminous Intensity (cd)		Color Chromaticity	Typical For	ward Vol	tage (V <sub>F</sub> )	
rait Nullibei	Bin Code	Min.	Max.	Coordinate	Bin Code	Min.	Max.
	K21	21.5	24		Y3	2.9	3.0
CTMOCODA	K24	K24 24	26	Defeate near 10	Z1	3.0	3.1
STW8C2PA	K26	26	28.5	Refer to page.12	Z2	3.1	3.2
	K28	28.5	31.2		Z3	3.2	3.3

### Table 5. Intensity rank distribution

Available ranks

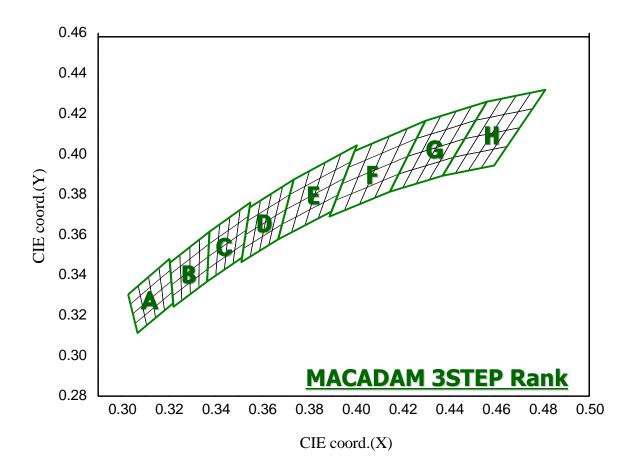
ССТ	CIE		IV F	Rank	
6,000 ~ 7,000K	Α	K21	K24	K26	K28
5,300 ~ 6,000K	В	K21	K24	K26	K28
4,700 ~ 5,300K	С	K21	K24	K26	K28
4,200 ~ 4,700K	D	K21	K24	K26	K28
3,700 ~ 4,200K	E	K21	K24	K26	K28
3,200 ~ 3,700K	F	K21	K24	K26	K28
2,900 ~ 3,200K	G	K21	K24	K26	K28
2,600 ~ 2,900K	Н	K21	K24	K26	K28

#### \*Notes:

All measurements were made under the standardized environment of SSC. In order to ensure availability, single color rank will not be orderable.

## **Color Bin Structure**

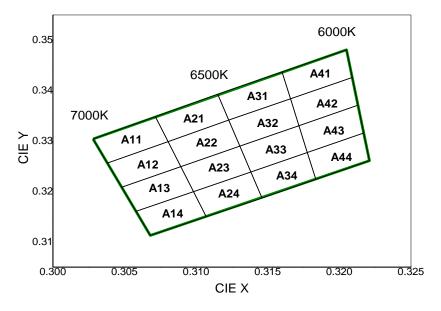
### CIE Chromaticity Diagram, T<sub>i</sub> = 25 °C, I<sub>F</sub> = 200mA



<sup>•</sup> Energy Star binning applied to all 2600~7000K.

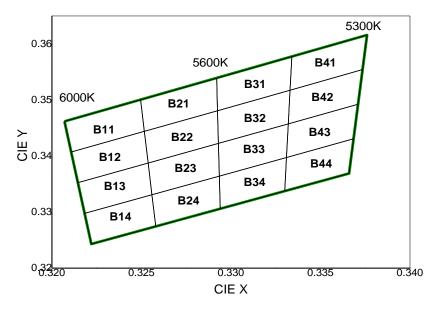
<sup>•</sup> Measurement Uncertainty of the Color Coordinates :  $\pm~0.005$ 

## **Color Bin Structure**



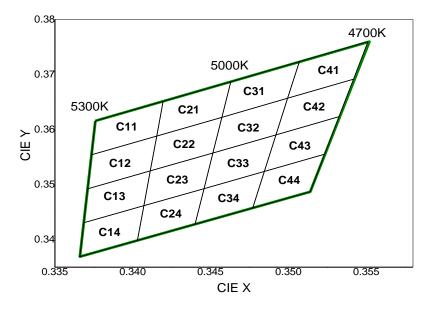
A11		A21		A3	A31		A41	
CIE X	CIE Y	CIE X	CIE Y	CIE X	CIE Y	CIE X	CIE Y	
0.3028	0.3304	0.3072	0.3349	0.3115	0.3393	0.3160	0.3437	
0.3038	0.3256	0.3080	0.3299	0.3123	0.3342	0.3166	0.3384	
0.3080	0.3299	0.3123	0.3342	0.3166	0.3384	0.3209	0.3426	
0.3072	0.3349	0.3115	0.3393	0.3160	0.3437	0.3205	0.3481	
A1	12	A:	22	A3	32	A	42	
CIE X	CIE Y	CIE X	CIE Y	CIE X	CIE Y	CIE X	CIE Y	
0.3038	0.3256	0.3080	0.3299	0.3123	0.3342	0.3166	0.3384	
0.3048	0.3209	0.3089	0.3249	0.3131	0.3290	0.3172	0.3331	
0.3089	0.3249	0.3131	0.3290	0.3172	0.3331	0.3213	0.3371	
0.3080	0.3299	0.3123	0.3342	0.3166	0.3384	0.3209	0.3426	
<b>A</b> 1	12	Α.	22				10	
A	13	A.	23	A3	33	A	43	
CIE X	CIE Y	CIE X	CIE Y	CIE X	CIE Y	CIE X	CIE Y	
	•						•	
CIE X	CIE Y	CIE X	CIE Y	CIE X	CIE Y	CIE X	CIE Y	
CIE X 0.3048	CIE Y 0.3209	CIE X 0.3089	CIE Y 0.3249	CIE X 0.3131	CIE Y 0.3290	CIE X 0.3172	CIE Y 0.3331	
CIE X 0.3048 0.3058	CIE Y 0.3209 0.3161	CIE X 0.3089 0.3098	CIE Y 0.3249 0.3200	CIE X 0.3131 0.3138	CIE Y 0.3290 0.3239	CIE X 0.3172 0.3178	CIE Y 0.3331 0.3277	
CIE X 0.3048 0.3058 0.3098	CIE Y 0.3209 0.3161 0.3200 0.3249	CIE X 0.3089 0.3098 0.3138 0.3131	CIE Y 0.3249 0.3200 0.3239	CIE X 0.3131 0.3138 0.3178	CIE Y 0.3290 0.3239 0.3277 0.3331	CIE X 0.3172 0.3178 0.3217 0.3213	CIE Y 0.3331 0.3277 0.3316	
CIE X 0.3048 0.3058 0.3098 0.3089	CIE Y 0.3209 0.3161 0.3200 0.3249	CIE X 0.3089 0.3098 0.3138 0.3131	CIE Y 0.3249 0.3200 0.3239 0.3290	CIE X 0.3131 0.3138 0.3178 0.3172	CIE Y 0.3290 0.3239 0.3277 0.3331	CIE X 0.3172 0.3178 0.3217 0.3213	CIE Y 0.3331 0.3277 0.3316 0.3371	
CIE X 0.3048 0.3058 0.3098 0.3089	CIE Y 0.3209 0.3161 0.3200 0.3249	CIE X 0.3089 0.3098 0.3138 0.3131	CIE Y 0.3249 0.3200 0.3239 0.3290	CIE X 0.3131 0.3138 0.3178 0.3172	CIE Y 0.3290 0.3239 0.3277 0.3331	CIE X 0.3172 0.3178 0.3217 0.3213	CIE Y 0.3331 0.3277 0.3316 0.3371	
CIE X 0.3048 0.3058 0.3098 0.3089	CIE Y 0.3209 0.3161 0.3200 0.3249 14 CIE Y	CIE X 0.3089 0.3098 0.3138 0.3131	CIE Y 0.3249 0.3200 0.3239 0.3290 24 CIE Y	CIE X 0.3131 0.3138 0.3178 0.3172 A3	CIE Y 0.3290 0.3239 0.3277 0.3331	CIE X 0.3172 0.3178 0.3217 0.3213 ACCIE X	CIE Y 0.3331 0.3277 0.3316 0.3371 44 CIE Y	
CIE X 0.3048 0.3058 0.3098 0.3089 A1 CIE X 0.3058	CIE Y 0.3209 0.3161 0.3200 0.3249 14 CIE Y 0.3161	CIE X 0.3089 0.3098 0.3138 0.3131 A: CIE X 0.3098	CIE Y 0.3249 0.3200 0.3239 0.3290 24 CIE Y 0.3200	CIE X 0.3131 0.3138 0.3178 0.3172 A3 CIE X 0.3138	CIE Y 0.3290 0.3239 0.3277 0.3331 34 CIE Y 0.3239	CIE X 0.3172 0.3178 0.3217 0.3213 ACCIE X 0.3178	CIE Y 0.3331 0.3277 0.3316 0.3371 44 CIE Y 0.3277	
CIE X 0.3048 0.3058 0.3098 0.3089  A1 CIE X 0.3058 0.3068	CIE Y 0.3209 0.3161 0.3200 0.3249 14 CIE Y 0.3161 0.3113	CIE X 0.3089 0.3098 0.3138 0.3131  AI  CIE X 0.3098 0.3107	CIE Y 0.3249 0.3200 0.3239 0.3290 24 CIE Y 0.3200 0.3150	CIE X 0.3131 0.3138 0.3178 0.3172  A3 CIE X 0.3138 0.3146	CIE Y 0.3290 0.3239 0.3277 0.3331 34 CIE Y 0.3239 0.3187	CIE X 0.3172 0.3178 0.3217 0.3213  ACCIE X 0.3178 0.3178	CIE Y 0.3331 0.3277 0.3316 0.3371 44 CIE Y 0.3277 0.3224	

## **Color Bin Structure**



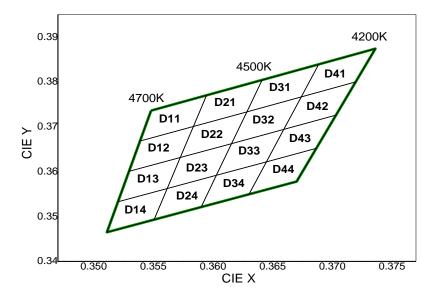
B1	11	B	21	B3	31	B	41
CIE X	CIE Y	CIE X	CIE Y	CIE X	CIE Y	CIE X	CIE Y
0.3207	0.3462	0.3250	0.3501	0.3292	0.3539	0.3334	0.3578
0.3211	0.3407	0.3252	0.3444	0.3293	0.3481	0.3333	0.3518
0.3252	0.3444	0.3293	0.3481	0.3333	0.3518	0.3374	0.3554
0.3250	0.3501	0.3292	0.3539	0.3334	0.3578	0.3376	0.3616
B1	12	В	22	В3	32	В	42
CIE X	CIE Y	CIE X	CIE Y	CIE X	CIE Y	CIE X	CIE Y
0.3211	0.3407	0.3252	0.3444	0.3293	0.3481	0.3333	0.3518
0.3215	0.3353	0.3254	0.3388	0.3293	0.3423	0.3332	0.3458
0.3254	0.3388	0.3293	0.3423	0.3332	0.3458	0.3371	0.3493
0.3252	0.3444	0.3293	0.3481	0.3333	0.3518	0.3374	0.3554
B1	10	-		-		_	
ь	3	В	23	B3	33	В-	43
CIE X	CIE Y	CIE X	CIE Y	CIE X	CIE Y	CIE X	CIE Y
	•						•
CIE X	CIE Y	CIE X	CIE Y	CIE X	CIE Y	CIE X	CIE Y
CIE X 0.3215	CIE Y 0.3353	CIE X 0.3254	CIE Y 0.3388	CIE X 0.3293	CIE Y 0.3423	CIE X 0.3332	CIE Y 0.3458
CIE X 0.3215 0.3218	CIE Y 0.3353 0.3298	CIE X 0.3254 0.3256	CIE Y 0.3388 0.3331	CIE X 0.3293 0.3294	CIE Y 0.3423 0.3364	CIE X 0.3332 0.3331	CIE Y 0.3458 0.3398
CIE X 0.3215 0.3218 0.3256	CIE Y 0.3353 0.3298 0.3331 0.3388	CIE X 0.3254 0.3256 0.3294 0.3293	CIE Y 0.3388 0.3331 0.3364	CIE X 0.3293 0.3294 0.3331	CIE Y 0.3423 0.3364 0.3398 0.3458	CIE X 0.3332 0.3331 0.3369	CIE Y 0.3458 0.3398 0.3431 0.3493
CIE X 0.3215 0.3218 0.3256 0.3254	CIE Y 0.3353 0.3298 0.3331 0.3388	CIE X 0.3254 0.3256 0.3294 0.3293	CIE Y 0.3388 0.3331 0.3364 0.3423	CIE X 0.3293 0.3294 0.3331 0.3332	CIE Y 0.3423 0.3364 0.3398 0.3458	CIE X 0.3332 0.3331 0.3369 0.3371	CIE Y 0.3458 0.3398 0.3431 0.3493
CIE X 0.3215 0.3218 0.3256 0.3254	CIE Y 0.3353 0.3298 0.3331 0.3388	CIE X 0.3254 0.3256 0.3294 0.3293	CIE Y 0.3388 0.3331 0.3364 0.3423	CIE X 0.3293 0.3294 0.3331 0.3332	CIE Y 0.3423 0.3364 0.3398 0.3458	CIE X 0.3332 0.3331 0.3369 0.3371	CIE Y 0.3458 0.3398 0.3431 0.3493
CIE X 0.3215 0.3218 0.3256 0.3254 B1	CIE Y 0.3353 0.3298 0.3331 0.3388 14 CIE Y	CIE X 0.3254 0.3256 0.3294 0.3293 B: CIE X	CIE Y 0.3388 0.3331 0.3364 0.3423 24 CIE Y	CIE X 0.3293 0.3294 0.3331 0.3332 B3	CIE Y 0.3423 0.3364 0.3398 0.3458	CIE X 0.3332 0.3331 0.3369 0.3371 BC CIE X	CIE Y 0.3458 0.3398 0.3431 0.3493 44 CIE Y
CIE X 0.3215 0.3218 0.3256 0.3254  B1 CIE X 0.3218	CIE Y 0.3353 0.3298 0.3331 0.3388 4 CIE Y 0.3298	CIE X 0.3254 0.3256 0.3294 0.3293 B: CIE X 0.3256	CIE Y 0.3388 0.3331 0.3364 0.3423 24 CIE Y 0.3331	CIE X 0.3293 0.3294 0.3331 0.3332 B3 CIE X 0.3294	CIE Y 0.3423 0.3364 0.3398 0.3458 4 CIE Y 0.3364	CIE X 0.3332 0.3331 0.3369 0.3371  B: CIE X 0.3331	CIE Y 0.3458 0.3398 0.3431 0.3493 44 CIE Y 0.3398
CIE X 0.3215 0.3218 0.3256 0.3254  B1 CIE X 0.3218 0.3222	CIE Y 0.3353 0.3298 0.3331 0.3388 4 CIE Y 0.3298 0.3243	CIE X 0.3254 0.3256 0.3294 0.3293  B: CIE X 0.3256 0.3256	CIE Y 0.3388 0.3331 0.3364 0.3423 24 CIE Y 0.3331 0.3275	CIE X 0.3293 0.3294 0.3331 0.3332 B3 CIE X 0.3294 0.3294	CIE Y 0.3423 0.3364 0.3398 0.3458 4 CIE Y 0.3364 0.3306	CIE X 0.3332 0.3331 0.3369 0.3371  B4 CIE X 0.3331 0.3330	CIE Y 0.3458 0.3398 0.3431 0.3493 44 CIE Y 0.3398 0.3338

## **Color Bin Structure**



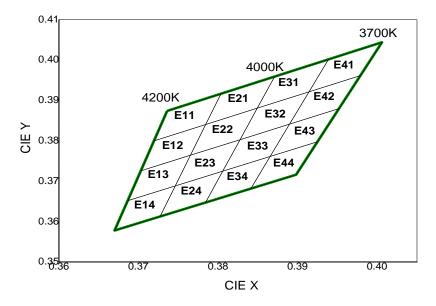
C	:11	C	21	C	31	C.	41
CIE X	CIE Y	CIE X	CIE Y	CIE X	CIE Y	CIE X	CIE Y
0.3376	0.3616	0.3420	0.3652	0.3463	0.3687	0.3507	0.3724
0.3374	0.3554	0.3415	0.3588	0.3457	0.3622	0.3500	0.3657
0.3415	0.3588	0.3457	0.3622	0.3500	0.3657	0.3542	0.3692
0.3420	0.3652	0.3463	0.3687	0.3507	0.3724	0.3551	0.3760
C	12	С	22	C	32	C	42
CIE X	CIE Y	CIE X	CIE Y	CIE X	CIE Y	CIE X	CIE Y
0.3374	0.3554	0.3415	0.3588	0.3457	0.3622	0.3500	0.3657
0.3371	0.3493	0.3411	0.3525	0.3452	0.3558	0.3492	0.3591
0.3411	0.3525	0.3452	0.3558	0.3492	0.3591	0.3533	0.3624
0.3415	0.3588	0.3457	0.3622	0.3500	0.3657	0.3542	0.3692
		_				_	
C	:13	C	23	C	33	C.	43
CIE X	CIE Y	CIE X	CIE Y	CIE X	CIE Y	CIE X	CIE Y
							•
CIE X	CIE Y	CIE X	CIE Y	CIE X	CIE Y	CIE X	CIE Y
CIE X 0.3371	CIE Y 0.3493	CIE X 0.3411	CIE Y 0.3525	CIE X 0.3452	CIE Y 0.3558	CIE X 0.3492	CIE Y 0.3591
CIE X 0.3371 0.3369	CIE Y 0.3493 0.3431	CIE X 0.3411 0.3407	CIE Y 0.3525 0.3462	CIE X 0.3452 0.3446	CIE Y 0.3558 0.3493	CIE X 0.3492 0.3485	CIE Y 0.3591 0.3524
CIE X 0.3371 0.3369 0.3407 0.3411	CIE Y 0.3493 0.3431 0.3462	CIE X 0.3411 0.3407 0.3446 0.3452	CIE Y 0.3525 0.3462 0.3493	CIE X 0.3452 0.3446 0.3485	CIE Y 0.3558 0.3493 0.3524 0.3591	CIE X 0.3492 0.3485 0.3523 0.3533	CIE Y 0.3591 0.3524 0.3555
CIE X 0.3371 0.3369 0.3407 0.3411	CIE Y 0.3493 0.3431 0.3462 0.3525	CIE X 0.3411 0.3407 0.3446 0.3452	CIE Y 0.3525 0.3462 0.3493 0.3558	CIE X 0.3452 0.3446 0.3485 0.3492	CIE Y 0.3558 0.3493 0.3524 0.3591	CIE X 0.3492 0.3485 0.3523 0.3533	CIE Y 0.3591 0.3524 0.3555 0.3624
CIE X 0.3371 0.3369 0.3407 0.3411	CIE Y 0.3493 0.3431 0.3462 0.3525	CIE X 0.3411 0.3407 0.3446 0.3452	CIE Y 0.3525 0.3462 0.3493 0.3558	CIE X 0.3452 0.3446 0.3485 0.3492	CIE Y 0.3558 0.3493 0.3524 0.3591	CIE X 0.3492 0.3485 0.3523 0.3533	CIE Y 0.3591 0.3524 0.3555 0.3624
CIE X 0.3371 0.3369 0.3407 0.3411	CIE Y 0.3493 0.3431 0.3462 0.3525	CIE X 0.3411 0.3407 0.3446 0.3452 CIE X	CIE Y 0.3525 0.3462 0.3493 0.3558 24 CIE Y	CIE X 0.3452 0.3446 0.3485 0.3492	CIE Y 0.3558 0.3493 0.3524 0.3591 34 CIE Y	CIE X 0.3492 0.3485 0.3523 0.3533 CIE X	CIE Y 0.3591 0.3524 0.3555 0.3624 44 CIE Y
CIE X 0.3371 0.3369 0.3407 0.3411  CIE X 0.3369	CIE Y 0.3493 0.3431 0.3462 0.3525 C14 CIE Y 0.3431	CIE X 0.3411 0.3407 0.3446 0.3452 CIE X 0.3407	CIE Y 0.3525 0.3462 0.3493 0.3558 24 CIE Y 0.3462	CIE X 0.3452 0.3446 0.3485 0.3492 CIE X 0.3446	CIE Y 0.3558 0.3493 0.3524 0.3591 84 CIE Y 0.3493	CIE X 0.3492 0.3485 0.3523 0.3533 CIE X 0.3485	CIE Y 0.3591 0.3524 0.3555 0.3624 44 CIE Y 0.3524
CIE X 0.3371 0.3369 0.3407 0.3411  CIE X 0.3369 0.3366	CIE Y 0.3493 0.3431 0.3462 0.3525 C14 CIE Y 0.3431 0.3369	CIE X 0.3411 0.3407 0.3446 0.3452  CIE X 0.3407 0.3403	CIE Y 0.3525 0.3462 0.3493 0.3558 24 CIE Y 0.3462 0.3399	CIE X 0.3452 0.3446 0.3485 0.3492 CIE X 0.3446 0.3440	CIE Y 0.3558 0.3493 0.3524 0.3591 34 CIE Y 0.3493 0.3428	CIE X 0.3492 0.3485 0.3523 0.3533  CIE X 0.3485 0.3477	CIE Y 0.3591 0.3524 0.3555 0.3624 44 CIE Y 0.3524 0.3458

## **Color Bin Structure**



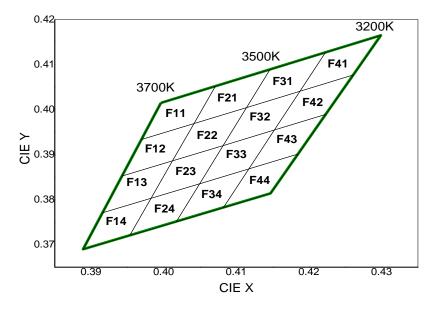
D	11	D	21	Da	51	D <sub>4</sub>	¥1
CIE X	CIE Y	CIE X	CIE Y	CIE X	CIE Y	CIE X	CIE Y
0.3548	0.3736	0.3595	0.3770	0.3641	0.3804	0.3689	0.3839
0.3539	0.3668	0.3584	0.3701	0.3628	0.3733	0.3674	0.3767
0.3584	0.3701	0.3628	0.3733	0.3674	0.3767	0.3720	0.3800
0.3595	0.3770	0.3641	0.3804	0.3689	0.3839	0.3736	0.3874
D	12	D	22	D3	32	D	<b>1</b> 2
CIE X	CIE Y	CIE X	CIE Y	CIE X	CIE Y	CIE X	CIE Y
0.3539	0.3668	0.3584	0.3701	0.3628	0.3733	0.3674	0.3767
0.3530	0.3601	0.3573	0.3632	0.3616	0.3663	0.3659	0.3694
0.3573	0.3632	0.3616	0.3663	0.3659	0.3694	0.3703	0.3726
0.3584	0.3701	0.3628	0.3733	0.3674	0.3767	0.3720	0.3800
D	13	D	23	D3	33	D <sub>4</sub>	13
CIE X	CIE Y	CIE X	CIE Y	CIE X	CIE Y	CIE X	CIE Y
0.3530	0.3601	0.3573	0.3632	0.3616	0.3663	0.3659	0.3694
0.3520	0.3533	0.3562	0.3562	0.3603	0.3592	0.3645	0.3622
0.3562							
	0.3562	0.3603	0.3592	0.3645	0.3622	0.3687	0.3652
0.3573	0.3562 0.3632	0.3603 0.3616	0.3592 0.3663	0.3645 0.3659	0.3622 0.3694	0.3687 0.3703	0.3652 0.3726
0.3573		0.3616			0.3694		0.3726
0.3573	0.3632	0.3616	0.3663	0.3659	0.3694	0.3703	0.3726
0.3573	0.3632	0.3616 D	0.3663	0.3659 D3	0.3694	0.3703 D	0.3726
0.3573 D CIE X	0.3632 14 CIE Y	0.3616 D: CIE X	0.3663 24 CIE Y	0.3659 D3	0.3694 84 CIE Y	0.3703 D4	0.3726 14 CIE Y
0.3573 D CIE X 0.3520	0.3632 14 CIE Y 0.3533	0.3616 D: CIE X 0.3562	0.3663 24 CIE Y 0.3562	0.3659  CIE X 0.3603	0.3694 84 CIE Y 0.3592	0.3703 D4 CIE X 0.3645	0.3726 44 CIE Y 0.3622
0.3573 CIE X 0.3520 0.3511	0.3632 14 CIE Y 0.3533 0.3465	0.3616  CIE X 0.3562 0.3551	0.3663 24 CIE Y 0.3562 0.3493	0.3659  CIE X 0.3603 0.3590	0.3694 64 CIE Y 0.3592 0.3521	0.3703 D4 CIE X 0.3645 0.3630	0.3726 44 CIE Y 0.3622 0.3550

## **Color Bin Structure**



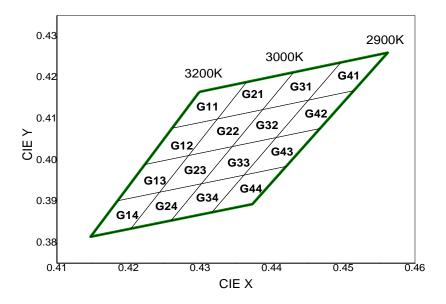
E'	11	E	21	E3	31	E	41
CIE X	CIE Y	CIE X	CIE Y	CIE X	CIE Y	CIE X	CIE Y
0.3736	0.3874	0.3804	0.3917	0.3871	0.3959	0.3939	0.4002
0.3720	0.3800	0.3784	0.3841	0.3849	0.3881	0.3914	0.3922
0.3784	0.3841	0.3849	0.3881	0.3914	0.3922	0.3979	0.3962
0.3804	0.3917	0.3871	0.3959	0.3939	0.4002	0.4006	0.4044
E.	12	E	22	E3	32	E-	42
CIE X	CIE Y	CIE X	CIE Y	CIE X	CIE Y	CIE X	CIE Y
0.3720	0.3800	0.3784	0.3841	0.3849	0.3881	0.3914	0.3922
0.3703	0.3726	0.3765	0.3765	0.3828	0.3803	0.3890	0.3842
0.3765	0.3765	0.3828	0.3803	0.3890	0.3842	0.3952	0.3880
0.3784	0.3841	0.3849	0.3881	0.3914	0.3922	0.3979	0.3962
E <sup>r</sup>	13	E	23	E3	3	E.	43
CIE X	CIE Y	CIE X	CIE Y	CIE X	CIE Y	CIE X	CIE Y
							•
CIE X	CIE Y	CIE X	CIE Y	CIE X	CIE Y	CIE X	CIE Y
CIE X 0.3703	CIE Y 0.3726	CIE X 0.3765	CIE Y 0.3765	CIE X 0.3828	CIE Y 0.3803	CIE X 0.3890	CIE Y 0.3842
CIE X 0.3703 0.3687	CIE Y 0.3726 0.3652	CIE X 0.3765 0.3746	CIE Y 0.3765 0.3689	CIE X 0.3828 0.3806	CIE Y 0.3803 0.3725	CIE X 0.3890 0.3865	CIE Y 0.3842 0.3762
CIE X 0.3703 0.3687 0.3746	CIE Y 0.3726 0.3652 0.3689 0.3765	CIE X 0.3765 0.3746 0.3806 0.3828	CIE Y 0.3765 0.3689 0.3725	CIE X 0.3828 0.3806 0.3865	CIE Y 0.3803 0.3725 0.3762 0.3842	CIE X 0.3890 0.3865 0.3925 0.3952	CIE Y 0.3842 0.3762 0.3798
CIE X 0.3703 0.3687 0.3746 0.3765	CIE Y 0.3726 0.3652 0.3689 0.3765	CIE X 0.3765 0.3746 0.3806 0.3828	CIE Y 0.3765 0.3689 0.3725 0.3803	CIE X 0.3828 0.3806 0.3865 0.3890	CIE Y 0.3803 0.3725 0.3762 0.3842	CIE X 0.3890 0.3865 0.3925 0.3952	CIE Y 0.3842 0.3762 0.3798 0.3880
CIE X 0.3703 0.3687 0.3746 0.3765	CIE Y 0.3726 0.3652 0.3689 0.3765	CIE X 0.3765 0.3746 0.3806 0.3828	CIE Y 0.3765 0.3689 0.3725 0.3803	CIE X 0.3828 0.3806 0.3865 0.3890	CIE Y 0.3803 0.3725 0.3762 0.3842	CIE X 0.3890 0.3865 0.3925 0.3952	CIE Y 0.3842 0.3762 0.3798 0.3880
CIE X 0.3703 0.3687 0.3746 0.3765	CIE Y 0.3726 0.3652 0.3689 0.3765	CIE X 0.3765 0.3746 0.3806 0.3828	CIE Y 0.3765 0.3689 0.3725 0.3803 24 CIE Y	CIE X 0.3828 0.3806 0.3865 0.3890  ESCUE X	CIE Y 0.3803 0.3725 0.3762 0.3842 CIE Y	CIE X 0.3890 0.3865 0.3925 0.3952	CIE Y 0.3842 0.3762 0.3798 0.3880 44 CIE Y
CIE X 0.3703 0.3687 0.3746 0.3765 E CIE X 0.3687	CIE Y 0.3726 0.3652 0.3689 0.3765 14 CIE Y 0.3652	CIE X 0.3765 0.3746 0.3806 0.3828  ESCIE X 0.3746	CIE Y 0.3765 0.3689 0.3725 0.3803 24 CIE Y 0.3689	CIE X 0.3828 0.3806 0.3865 0.3890 ES CIE X 0.3806	CIE Y 0.3803 0.3725 0.3762 0.3842 4 CIE Y 0.3725	CIE X 0.3890 0.3865 0.3925 0.3952 E CIE X 0.3865	CIE Y 0.3842 0.3762 0.3798 0.3880 44 CIE Y 0.3762
CIE X 0.3703 0.3687 0.3746 0.3765  E CIE X 0.3687 0.3670	CIE Y 0.3726 0.3652 0.3689 0.3765 14 CIE Y 0.3652 0.3578	CIE X 0.3765 0.3746 0.3806 0.3828  EXECUTE X 0.3746 0.3727	CIE Y 0.3765 0.3689 0.3725 0.3803 24 CIE Y 0.3689 0.3613	CIE X 0.3828 0.3806 0.3865 0.3890 E3 CIE X 0.3806 0.3784	CIE Y 0.3803 0.3725 0.3762 0.3842 4 CIE Y 0.3725 0.3647	CIE X 0.3890 0.3865 0.3925 0.3952  E-  CIE X 0.3865 0.3841	CIE Y 0.3842 0.3762 0.3798 0.3880 44 CIE Y 0.3762 0.3682

## **Color Bin Structure**



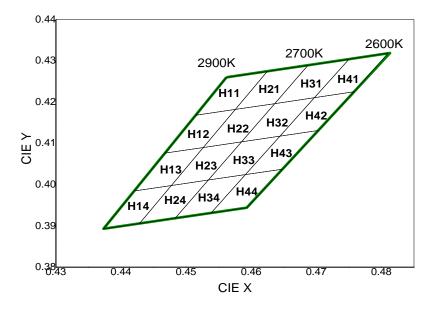
Г	1	F:	21	F3	31	F <sub>4</sub>	41
CIE X	CIE Y	CIE X	CIE Y	CIE X	CIE Y	CIE X	CIE Y
0.3996	0.4015	0.4071	0.4052	0.4146	0.4089	0.4223	0.4127
0.3969	0.3934	0.4042	0.3969	0.4114	0.4005	0.4187	0.4041
0.4042	0.3969	0.4114	0.4005	0.4187	0.4041	0.4261	0.4077
0.4071	0.4052	0.4146	0.4089	0.4223	0.4127	0.4299	0.4165
F1	2	F:	22	F3	2	F-	42
CIE X	CIE Y	CIE X	CIE Y	CIE X	CIE Y	CIE X	CIE Y
0.3969	0.3934	0.4042	0.3969	0.4114	0.4005	0.4187	0.4041
0.3943	0.3853	0.4012	0.3886	0.4082	0.3920	0.4152	0.3955
0.4012	0.3886	0.4082	0.3920	0.4152	0.3955	0.4223	0.3990
0.4042	0.3969	0.4114	0.4005	0.4187	0.4041	0.4261	0.4077
F1	3	F:	23	F3	3	F <sub>4</sub>	43
CIE X	CIE Y	CIE X	CIE Y	CIE X	CIE Y	CIE X	CIE Y
							•
CIE X	CIE Y	CIE X	CIE Y	CIE X	CIE Y	CIE X	CIE Y
CIE X 0.3943	CIE Y 0.3853	CIE X 0.4012	CIE Y 0.3886	CIE X 0.4082	CIE Y 0.3920	CIE X 0.4152	CIE Y 0.3955
CIE X 0.3943 0.3916	CIE Y 0.3853 0.3771	CIE X 0.4012 0.3983	CIE Y 0.3886 0.3803	CIE X 0.4082 0.4049	CIE Y 0.3920 0.3836	CIE X 0.4152 0.4117	CIE Y 0.3955 0.3869
CIE X 0.3943 0.3916 0.3983	CIE Y 0.3853 0.3771 0.3803 0.3886	CIE X 0.4012 0.3983 0.4049 0.4082	CIE Y 0.3886 0.3803 0.3836	CIE X 0.4082 0.4049 0.4117	CIE Y 0.3920 0.3836 0.3869 0.3955	CIE X 0.4152 0.4117 0.4185 0.4223	CIE Y 0.3955 0.3869 0.3902
CIE X 0.3943 0.3916 0.3983 0.4012	CIE Y 0.3853 0.3771 0.3803 0.3886	CIE X 0.4012 0.3983 0.4049 0.4082	CIE Y 0.3886 0.3803 0.3836 0.3920	CIE X 0.4082 0.4049 0.4117 0.4152	CIE Y 0.3920 0.3836 0.3869 0.3955	CIE X 0.4152 0.4117 0.4185 0.4223	CIE Y 0.3955 0.3869 0.3902 0.3990
CIE X 0.3943 0.3916 0.3983 0.4012	CIE Y 0.3853 0.3771 0.3803 0.3886	CIE X 0.4012 0.3983 0.4049 0.4082	CIE Y 0.3886 0.3803 0.3836 0.3920	CIE X 0.4082 0.4049 0.4117 0.4152	CIE Y 0.3920 0.3836 0.3869 0.3955	CIE X 0.4152 0.4117 0.4185 0.4223	CIE Y 0.3955 0.3869 0.3902 0.3990
CIE X 0.3943 0.3916 0.3983 0.4012  F1 CIE X	CIE Y 0.3853 0.3771 0.3803 0.3886 4 CIE Y	CIE X 0.4012 0.3983 0.4049 0.4082	CIE Y 0.3886 0.3803 0.3836 0.3920 24 CIE Y	CIE X 0.4082 0.4049 0.4117 0.4152 F3	CIE Y 0.3920 0.3836 0.3869 0.3955	CIE X 0.4152 0.4117 0.4185 0.4223	CIE Y 0.3955 0.3869 0.3902 0.3990 44 CIE Y
CIE X 0.3943 0.3916 0.3983 0.4012  F1 CIE X 0.3916	CIE Y 0.3853 0.3771 0.3803 0.3886 4 CIE Y 0.3771	CIE X 0.4012 0.3983 0.4049 0.4082  F: CIE X 0.3983	CIE Y 0.3886 0.3803 0.3836 0.3920 24 CIE Y 0.3803	CIE X 0.4082 0.4049 0.4117 0.4152  F3 CIE X 0.4049	CIE Y 0.3920 0.3836 0.3869 0.3955 4 CIE Y 0.3836	CIE X 0.4152 0.4117 0.4185 0.4223  F2 CIE X 0.4117	CIE Y 0.3955 0.3869 0.3902 0.3990 44 CIE Y 0.3869

## **Color Bin Structure**



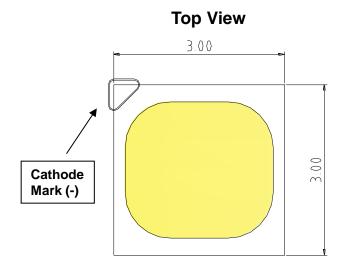
G	11	G	21	G	51	G	41
CIE X	CIE Y	CIE X	CIE Y	CIE X	CIE Y	CIE X	CIE Y
0.4299	0.4165	0.4364	0.4188	0.4430	0.4212	0.4496	0.4236
0.4261	0.4077	0.4324	0.4099	0.4387	0.4122	0.4451	0.4145
0.4324	0.4100	0.4387	0.4122	0.4451	0.4145	0.4514	0.4168
0.4365	0.4189	0.4430	0.4212	0.4496	0.4236	0.4562	0.4260
G <sup>2</sup>	12	G	22	GS	32	G	42
CIE X	CIE Y	CIE X	CIE Y	CIE X	CIE Y	CIE X	CIE Y
0.4261	0.4077	0.4324	0.4100	0.4387	0.4122	0.4451	0.4145
0.4223	0.3990	0.4284	0.4011	0.4345	0.4033	0.4406	0.4055
0.4284	0.4011	0.4345	0.4033	0.4406	0.4055	0.4468	0.4077
0.4324	0.4100	0.4387	0.4122	0.4451	0.4145	0.4515	0.4168
0	10	_	00	0.0	\ <u>0</u>	_	43
G	13	G	23	G	53	G	43
CIE X	CIE Y	CIE X	CIE Y	CIE X	CIE Y	CIE X	CIE Y
	•						
CIE X	CIE Y	CIE X	CIE Y	CIE X	CIE Y	CIE X	CIE Y
CIE X 0.4223	CIE Y 0.3990	CIE X 0.4284	CIE Y 0.4011	CIE X 0.4345	CIE Y 0.4033	CIE X 0.4406	CIE Y 0.4055
CIE X 0.4223 0.4185	CIE Y 0.3990 0.3902	CIE X 0.4284 0.4243	CIE Y 0.4011 0.3922	CIE X 0.4345 0.4302	CIE Y 0.4033 0.3943	CIE X 0.4406 0.4361	CIE Y 0.4055 0.3964
CIE X 0.4223 0.4185 0.4243	CIE Y 0.3990 0.3902 0.3922 0.4011	CIE X 0.4284 0.4243 0.4302 0.4345	CIE Y 0.4011 0.3922 0.3943	CIE X 0.4345 0.4302 0.4361	CIE Y 0.4033 0.3943 0.3964 0.4055	CIE X 0.4406 0.4361 0.4420 0.4468	CIE Y 0.4055 0.3964 0.3985
CIE X 0.4223 0.4185 0.4243 0.4284	CIE Y 0.3990 0.3902 0.3922 0.4011	CIE X 0.4284 0.4243 0.4302 0.4345	CIE Y 0.4011 0.3922 0.3943 0.4033	CIE X 0.4345 0.4302 0.4361 0.4406	CIE Y 0.4033 0.3943 0.3964 0.4055	CIE X 0.4406 0.4361 0.4420 0.4468	CIE Y 0.4055 0.3964 0.3985 0.4077
CIE X 0.4223 0.4185 0.4243 0.4284	CIE Y 0.3990 0.3902 0.3922 0.4011	CIE X 0.4284 0.4243 0.4302 0.4345	CIE Y 0.4011 0.3922 0.3943 0.4033	CIE X 0.4345 0.4302 0.4361 0.4406	CIE Y 0.4033 0.3943 0.3964 0.4055	CIE X 0.4406 0.4361 0.4420 0.4468	CIE Y 0.4055 0.3964 0.3985 0.4077
CIE X 0.4223 0.4185 0.4243 0.4284  CIE X	CIE Y 0.3990 0.3902 0.3922 0.4011 14 CIE Y	CIE X 0.4284 0.4243 0.4302 0.4345  G CIE X	CIE Y 0.4011 0.3922 0.3943 0.4033 24 CIE Y	CIE X 0.4345 0.4302 0.4361 0.4406	CIE Y 0.4033 0.3943 0.3964 0.4055	CIE X 0.4406 0.4361 0.4420 0.4468 G	CIE Y 0.4055 0.3964 0.3985 0.4077 44 CIE Y
CIE X 0.4223 0.4185 0.4243 0.4284  Gr CIE X 0.4243	CIE Y 0.3990 0.3902 0.3922 0.4011 14 CIE Y 0.3922	CIE X 0.4284 0.4243 0.4302 0.4345 G CIE X 0.4302	CIE Y 0.4011 0.3922 0.3943 0.4033 24 CIE Y 0.3943	CIE X 0.4345 0.4302 0.4361 0.4406 CIE X 0.4302	CIE Y 0.4033 0.3943 0.3964 0.4055 64 CIE Y 0.3943	CIE X 0.4406 0.4361 0.4420 0.4468 G CIE X 0.4361	CIE Y 0.4055 0.3964 0.3985 0.4077 44 CIE Y 0.3964
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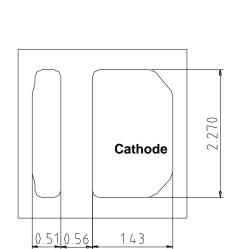
## **Color Bin Structure**



III.	11	H	21	H3	31	H-	41
CIE X	CIE Y	CIE X	CIE Y	CIE X	CIE Y	CIE X	CIE Y
0.4562	0.4260	0.4625	0.4275	0.4687	0.4289	0.4750	0.4304
0.4515	0.4168	0.4575	0.4182	0.4636	0.4197	0.4697	0.4211
0.4575	0.4182	0.4636	0.4197	0.4697	0.4211	0.4758	0.4225
0.4625	0.4275	0.4687	0.4289	0.4750	0.4304	0.4810	0.4319
H1	12	H:	22	H3	32	H	42
CIE X	CIE Y	CIE X	CIE Y	CIE X	CIE Y	CIE X	CIE Y
0.4515	0.4168	0.4575	0.4182	0.4636	0.4197	0.4697	0.4211
0.4468	0.4077	0.4526	0.4090	0.4585	0.4104	0.4644	0.4118
0.4526	0.4090	0.4585	0.4104	0.4644	0.4118	0.4703	0.4132
0.4575	0.4182	0.4636	0.4197	0.4697	0.4211	0.4758	0.4225
H1	13	H:	23	H3	3	H	43
CIE X	CIE Y	CIE X	CIE Y	CIE X	CIE Y	CIE X	CIE Y
			•				•
CIE X	CIE Y	CIE X	CIE Y	CIE X	CIE Y	CIE X	CIE Y
CIE X 0.4468	CIE Y 0.4077	CIE X 0.4526	CIE Y 0.4090	CIE X 0.4585	CIE Y 0.4104	CIE X 0.4644	CIE Y 0.4118
CIE X 0.4468 0.4420	CIE Y 0.4077 0.3985	CIE X 0.4526 0.4477	CIE Y 0.4090 0.3998	CIE X 0.4585 0.4534	CIE Y 0.4104 0.4012	CIE X 0.4644 0.4591	CIE Y 0.4118 0.4025
CIE X 0.4468 0.4420 0.4477	CIE Y 0.4077 0.3985 0.3998 0.4090	CIE X 0.4526 0.4477 0.4534 0.4585	CIE Y 0.4090 0.3998 0.4012	CIE X 0.4585 0.4534 0.4591	CIE Y 0.4104 0.4012 0.4025 0.4118	CIE X 0.4644 0.4591 0.4648 0.4703	CIE Y 0.4118 0.4025 0.4038
CIE X 0.4468 0.4420 0.4477 0.4526	CIE Y 0.4077 0.3985 0.3998 0.4090	CIE X 0.4526 0.4477 0.4534 0.4585	CIE Y 0.4090 0.3998 0.4012 0.4104	CIE X 0.4585 0.4534 0.4591 0.4644	CIE Y 0.4104 0.4012 0.4025 0.4118	CIE X 0.4644 0.4591 0.4648 0.4703	CIE Y 0.4118 0.4025 0.4038 0.4132
CIE X 0.4468 0.4420 0.4477 0.4526	CIE Y 0.4077 0.3985 0.3998 0.4090	CIE X 0.4526 0.4477 0.4534 0.4585	CIE Y 0.4090 0.3998 0.4012 0.4104	CIE X 0.4585 0.4534 0.4591 0.4644	CIE Y 0.4104 0.4012 0.4025 0.4118	CIE X 0.4644 0.4591 0.4648 0.4703	CIE Y 0.4118 0.4025 0.4038 0.4132
CIE X 0.4468 0.4420 0.4477 0.4526 H1 CIE X	CIE Y 0.4077 0.3985 0.3998 0.4090 14 CIE Y	CIE X 0.4526 0.4477 0.4534 0.4585	CIE Y 0.4090 0.3998 0.4012 0.4104 CIE Y	CIE X 0.4585 0.4534 0.4591 0.4644 H3	CIE Y 0.4104 0.4012 0.4025 0.4118 44 CIE Y	CIE X 0.4644 0.4591 0.4648 0.4703	CIE Y 0.4118 0.4025 0.4038 0.4132 44 CIE Y
CIE X 0.4468 0.4420 0.4477 0.4526 H1 CIE X 0.4420	CIE Y 0.4077 0.3985 0.3998 0.4090 4 CIE Y 0.3985	CIE X 0.4526 0.4477 0.4534 0.4585 H: CIE X 0.4477	CIE Y 0.4090 0.3998 0.4012 0.4104 24 CIE Y 0.3998	CIE X 0.4585 0.4534 0.4591 0.4644 H3 CIE X 0.4534	CIE Y 0.4104 0.4012 0.4025 0.4118 4 CIE Y 0.4012	CIE X 0.4644 0.4591 0.4648 0.4703 H CIE X 0.4591	CIE Y 0.4118 0.4025 0.4038 0.4132 44 CIE Y 0.4025

## **Mechanical Dimensions**

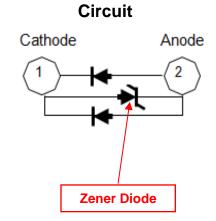




**Bottom View** 

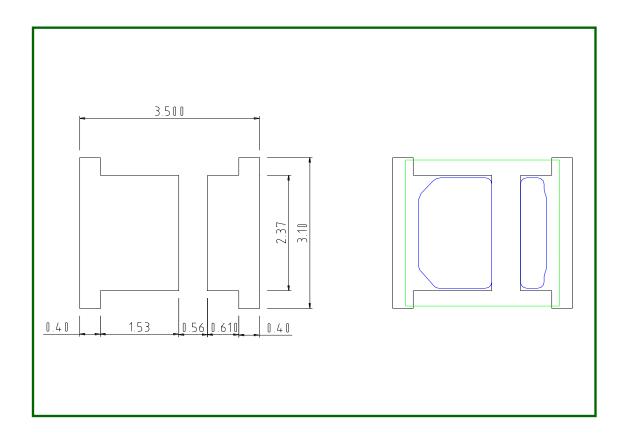


**Side View** 



- (1) All dimensions are in millimeters.
- (2) Scale: none
- (3) Undefined tolerance is  $\pm 0.2 mm$

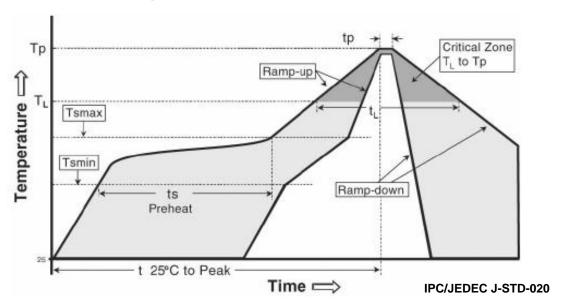
## **Recommended Solder Pad**



### Notes:

- (1) All dimensions are in millimeters.
- (2) Scale: none
- (3) This drawing without tolerances are for reference only
- (4) Undefined tolerance is  $\pm 0.1 \text{mm}$

## **Reflow Soldering Characteristics**

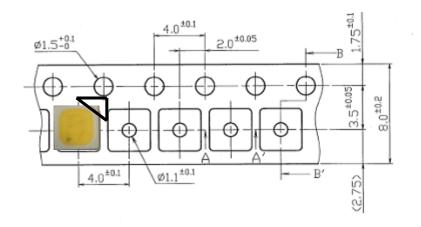


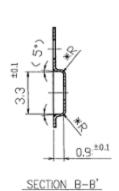
Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly
Average ramp-up rate (Tsmax to Tp)	3° C/second max.	3° C/second max.
Preheat - Temperature Min (Tsmin) - Temperature Max (Tsmax) - Time (Tsmin to Tsmax) (ts)	100 °C 150 °C 60-120 seconds	150 °C 200 °C 60-180 seconds
Time maintained above: - Temperature (TL) - Time (tL)	183 °C 60-150 seconds	217 °C 60-150 seconds
Peak Temperature (Tp)	215℃	260℃
Time within 5°C of actual Peak Temperature (tp)2	10-30 seconds	20-40 seconds
Ramp-down Rate	6 °C/second max.	6 °C/second max.
Time 25°C to Peak Temperature	6 minutes max.	8 minutes max.

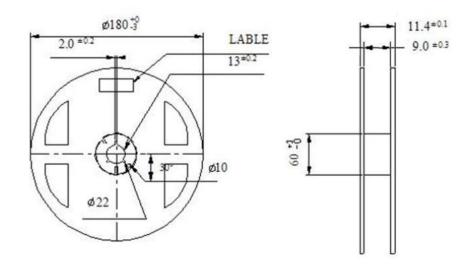
### Caution

- (1) Reflow soldering is recommended not to be done more than two times. In the case of more than 24 hours passed soldering after first, LEDs will be damaged.
- (2) Repairs should not be done after the LEDs have been soldered. When repair is unavoidable, suitable tools must be used.
- (3) Die slug is to be soldered.
- (4) When soldering, do not put stress on the LEDs during heating.
- (5) After soldering, do not warp the circuit board.

## **Emitter Tape & Reel Packaging**





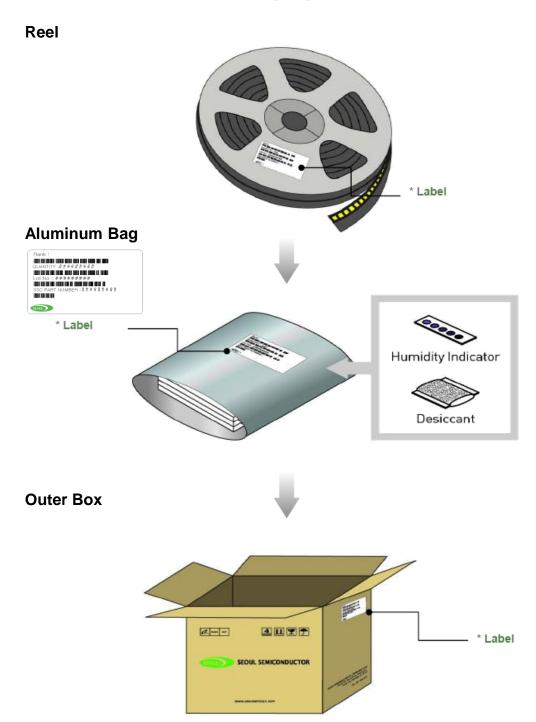


( Tolerance:  $\pm 0.2$ , Unit: mm )

- (1) Quantity: 4,500pcs/Reel
- (2) Cumulative Tolerance : Cumulative Tolerance/10 pitches to be  $\pm 0.2 \text{mm}$
- (3) Adhesion Strength of Cover Tape
  Adhesion strength to be 0.1-0.7N when the cover tape is turned off from the carrier tape at the angle of 10° to the carrier tape.
- (4) Package: P/N, Manufacturing data Code No. and Quantity to be indicated on a damp proof Package.



# **Emitter Tape & Reel Packaging**



## **Product Nomenclature**

Table 6. Part Numbering System :  $X_1X_2X_3X_4X_5X_6X_7X_8$ 

Part Number Code	Description	Part Number	Value
<b>X</b> <sub>1</sub>	Company	S	
X <sub>2</sub>	Top View LED series	Т	
X <sub>3</sub> X <sub>4</sub>	Color Specification	W8	CRI 80
X <sub>5</sub>	Package series	С	C series
X <sub>6</sub> X <sub>7</sub>	Characteristic code	2P	
X <sub>8</sub>	Revision	А	

Table 7. Lot Numbering System  $: Y_1Y_2Y_3Y_4Y_5Y_6Y_7Y_8Y_9Y_{10} - Y_{11}Y_{12}Y_{13}Y_{14}Y_{15}Y_{16}Y_{17}$ 

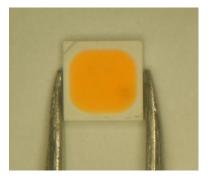
Lot Number Code	Description	Lot Number	Value
Y <sub>1</sub> Y <sub>2</sub>	Year		
Y <sub>3</sub>	Month		
Y <sub>4</sub> Y <sub>5</sub>	Day		
Y <sub>6</sub>	Top View LED series		
Y <sub>7</sub> Y <sub>8</sub> Y <sub>9</sub> Y <sub>10</sub>	Mass order		
Y <sub>11</sub> Y <sub>12</sub> Y <sub>13</sub> Y <sub>14</sub> Y <sub>15</sub> Y <sub>16</sub> Y <sub>17</sub>	Internal Number		

## **Handling of Silicone Resin for LEDs**

(1) During processing, mechanical stress on the surface should be minimized as much as possible. Sharp objects of all types should not be used to pierce the sealing compound.



(2) In general, LEDs should only be handled from the side. By the way, this also applies to LEDs without a silicone sealant, since the surface can also become scratched.



- (3) When populating boards in SMT production, there are basically no restrictions regarding the form of the pick and place nozzle, except that mechanical pressure on the surface of the resin must be prevented. This is assured by choosing a pick and place nozzle which is larger than the LED's reflector area.
- (4) Silicone differs from materials conventionally used for the manufacturing of LEDs. These conditions must be considered during the handling of such devices. Compared to standard encapsulants, silicone is generally softer, and the surface is more likely to attract dust.

As mentioned previously, the increased sensitivity to dust requires special care during processing. In cases where a minimal level of dirt and dust particles cannot be guaranteed, a suitable cleaning solution must be applied to the surface after the soldering of components.

- (5) SSC suggests using isopropyl alcohol for cleaning. In case other solvents are used, it must be assured that these solvents do not dissolve the package or resin.

  Ultrasonic cleaning is not recommended. Ultrasonic cleaning may cause damage to the LED.
- (6) Please do not mold this product into another resin (epoxy, urethane, etc) and do not handle this. product with acid or sulfur material in sealed space.

### **Precaution for Use**

(1) Storage

To avoid the moisture penetration, we recommend store in a dry box with a desiccant.

The recommended storage temperature range is 5  $^{\circ}$ C to 30  $^{\circ}$ C and a maximum humidity of RH50%.

(2) Use Precaution after Opening the Packaging

Use proper SMT techniques when the LED is to be soldered dipped as separation of the lens may affect the light output efficiency.

Pay attention to the following:

- a. Recommend conditions after opening the package
  - Sealing
  - Temperature : 5 ~ 30 °C Humidity : less than RH60%
- b. If the package has been opened more than 4 week(MSL\_2a) or the color of the desiccant changes, components should be dried for 10-24hr at  $65\pm5$  °C
- (3) Do not apply mechanical force or excess vibration during the cooling process to normal temperature after soldering.
- (4) Do not rapidly cool device after soldering.
- (5) Components should not be mounted on warped (non coplanar) portion of PCB.
- (6) Radioactive exposure is not considered for the products listed here in.
- (7) Gallium arsenide is used in some of the products listed in this publication.
  These products are dangerous if they are burned or shredded in the process of disposal.
  It is also dangerous to drink the liquid or inhale the gas generated by such products when chemically disposed of.
- (8) This device should not be used in any type of fluid such as water, oil, organic solvent and etc. When washing is required, IPA (Isopropyl Alcohol) should be used.
- (9) When the LEDs are in operation the maximum current should be decided after measuring the package temperature.

### **Precaution for Use**

- (10) The appearance and specifications of the product may be modified for improvement without notice.
- (11) Long time exposure of sunlight or occasional UV exposure will cause lens discoloration.
- (12) VOCs (Volatile organic compounds) emitted from materials used in the construction of fixtures can penetrate silicone encapsulants of LEDs and discolor when exposed to heat and photonic energy. The result can be a significant loss of light output from the fixture. Knowledge of the properties of the materials selected to be used in the construction of fixtures can help prevent these issues.
- (13) Attaching LEDs, do not use adhesives that outgas organic vapor.
- (14) The driving circuit must be designed to allow forward voltage only when it is ON or OFF.
  If the reverse voltage is applied to LED, migration can be generated resulting in LED damage.
- (15) Similar to most Solid state devices;
  LEDs are sensitive to Electro-Static Discharge (ESD) and Electrical Over Stress (EOS).
  Below is a list of suggestions that Seoul Semiconductor purposes to minimize these effects.
- a. ESD (Electro Static Discharge)

Electrostatic discharge (ESD) is the defined as the release of static electricity when two objects come into contact. While most ESD events are considered harmless, it can be an expensive problem in many industrial environments during production and storage. The damage from ESD to an LEDs may cause the product to demonstrate unusual characteristics such as:

- Increase in reverse leakage current lowered turn-on voltage
- Abnormal emissions from the LED at low current

The following recommendations are suggested to help minimize the potential for an ESD event. One or more recommended work area suggestions:

- Ionizing fan setup
- ESD table/shelf mat made of conductive materials
- ESD safe storage containers

One or more personnel suggestion options:

- Antistatic wrist-strap
- Antistatic material shoes
- Antistatic clothes

#### Environmental controls:

- Humidity control (ESD gets worse in a dry environment)



### **Precaution for Use**

#### b. EOS (Electrical Over Stress)

Electrical Over-Stress (EOS) is defined as damage that may occur when an electronic device is subjected to a current or voltage that is beyond the maximum specification limits of the device. The effects from an EOS event can be noticed through product performance like:

- Changes to the performance of the LED package
  (If the damage is around the bond pad area and since the package is completely encapsulated the package may turn on but flicker show severe performance degradation.)
- Changes to the light output of the luminaire from component failure
- Components on the board not operating at determined drive power

Failure of performance from entire fixture due to changes in circuit voltage and current across total circuit causing trickle down failures. It is impossible to predict the failure mode of every LED exposed to electrical overstress as the failure modes have been investigated to vary, but there are some common signs that will indicate an EOS event has occurred:

- Damaged may be noticed to the bond wires (appearing similar to a blown fuse)
- Damage to the bond pads located on the emission surface of the LED package (shadowing can be noticed around the bond pads while viewing through a microscope)
- Anomalies noticed in the encapsulation and phosphor around the bond wires
- This damage usually appears due to the thermal stress produced during the EOS event
- c. To help minimize the damage from an EOS event Seoul Semiconductor recommends utilizing:
  - A surge protection circuit
  - An appropriately rated over voltage protection device
  - A current limiting device



## **Company Information**

#### Published by

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### **Company Information**

Seoul Semiconductor (www.SeoulSemicon.com) manufacturers and packages a wide selection of light emitting diodes (LEDs) for the automotive, general illumination/lighting, Home appliance, signage and back lighting markets. The company is the world's fifth largest LED supplier, holding more than 10,000 patents globally, while offering a wide range of LED technology and production capacity in areas such as "nPola", "Acrich", the world's first commercially produced AC LED, and "Acrich MJT - Multi-Junction Technology" a proprietary family of high-voltage LEDs.

The company's broad product portfolio includes a wide array of package and device choices such as Acrich and Acirch2, high-brightness LEDs, mid-power LEDs, side-view LEDs, and through-hole type LEDs as well as custom modules, displays, and sensors.

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